

CLAIMS

- 5 1. An image-processing apparatus comprising:
- n image processing sections which receive n (n represents an integer equal to or larger than "2") consecutive pixel data items that are respectively input with the same timing and which respectively process the respective input pixel data items with the same timing; and
- a control section for controlling the n image processing sections, wherein each of the image processing sections are capable of being set to one of a first operation mode allowing data communication with the controlling section and a
- 10 second operation mode allowing only reception from the controlling section, one of the image processing sections is set to the first operation mode, and n-1 of the image processing sections are set to the second operation mode;
- commands are commonly given to the n image processing sections from the controlling section; and
- 15 when a command is given from the controlling section to the one of the image processing sections that is set to the first operation mode, the n image processing sections individually execute the same processing with the same timing.
2. An image-processing apparatus according to Claim 1, wherein the n image processing sections are allocated in the same address space in address spaces that can
- 20 be controlled by the controlling section.
3. An image-processing apparatus according to ^{Claim 1} ~~one of Claims 1 and 2~~, wherein each of the image processing sections comprises a mode-setting terminal for setting one of the first operation mode and the second operation mode, and one of the operation modes is set according to a mode-setting signal input to the mode-setting terminal.
- 25 4. An image-processing apparatus according to ^{Claim 1} ~~one of Claims 1 to 3~~, comprising a memory for storing image-processing data commonly used by the respective image processing sections,
- wherein the image processing section set to the first operation mode can write the image-processing data, which is fed from the controlling section, to the memory, and in addition, can read out the image-processing data written in the memory; and
- 30 the image processing section set to the second operation mode can input the image-processing data read out by the image processing section set to the first operation mode from the memory.

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Claim 1

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